PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re U.S. I	Patent Application	
Applicant:	Nishida et al.) I hereby certify that this paper is being deposited with the United States Postal Service as first-class mail in an
Serial No.	09/960,094	envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on this date.
Filed:	September 21, 2001	6 Nova DyB
	UCTANCE DEVICE DRIVING TEM, INFORMATION	Date) F-CLASS.CER Registration No 29367) Attorney for Applicant
STO	RAGE APPARATUS, AND	· ·
IND	UCTANCE DEVICE DRIVING	·)
MET	THOD) Co.
Art Unit:	2651	Technology 2000
	SUBMISSION OF SUBSTITUT	

Assistant Commissioner for Patents Washington, DC 20231

Sir:

0941.65858

Please approve the attached substitute formal drawings, which are the originals of the facsimile drawings filed with this application. No new matter has been added.

Respectfully submitted,

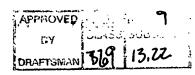
GREER, BURNS & CRAIN, LTD.

By Patrick G. Burns

Registration No. 29,367

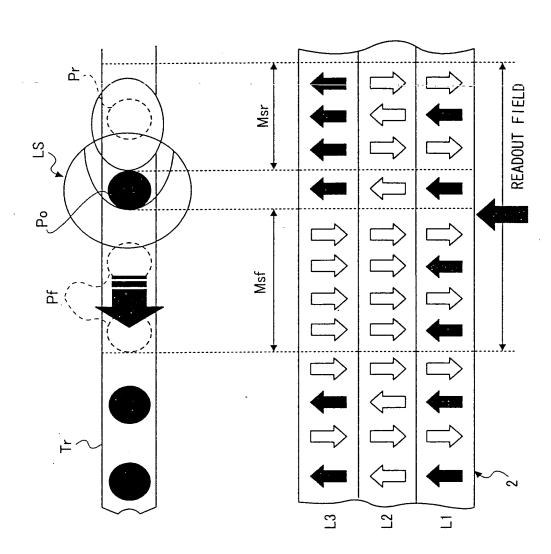
November 6, 2001 300 South Wacker Drive Suite 2500 Chicago, Illinois 60606 Telephone: (312) 360-0080 Facsimile: (312) 360-9315

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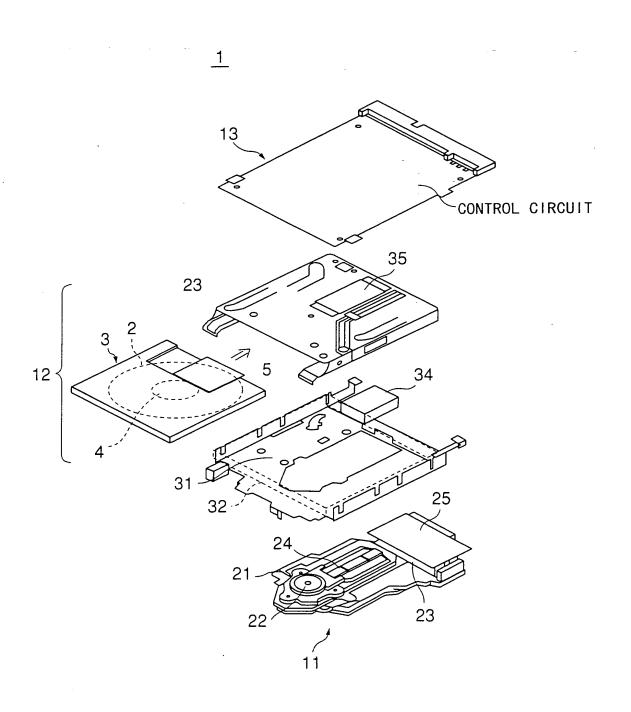


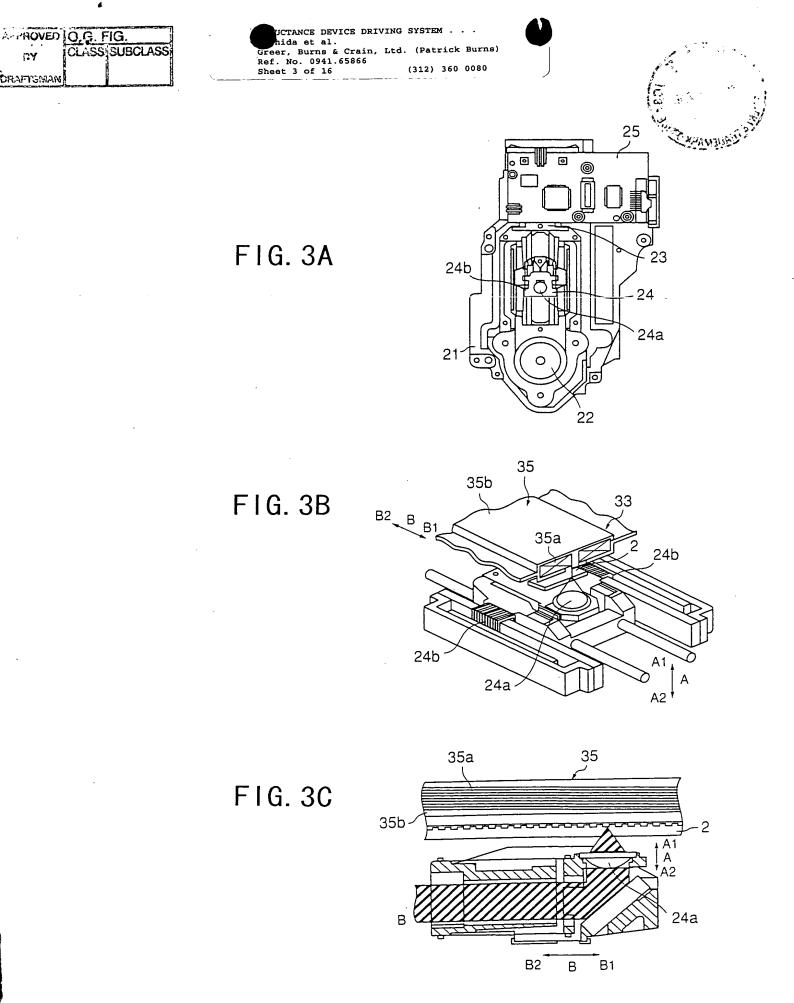
F1G. 1A

APPROVED O.G. FIG.
CLASS SUBCLASS
DRAFTSMAN



FIG. 2





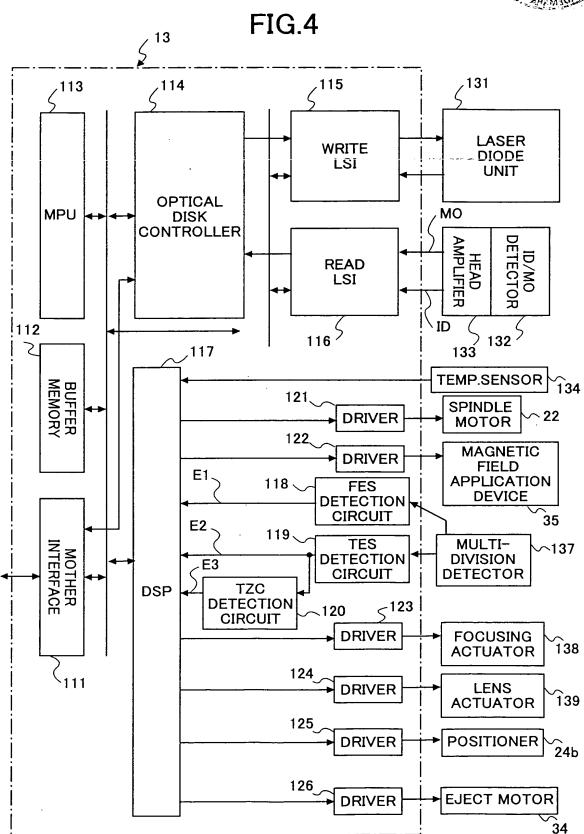
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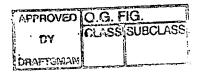
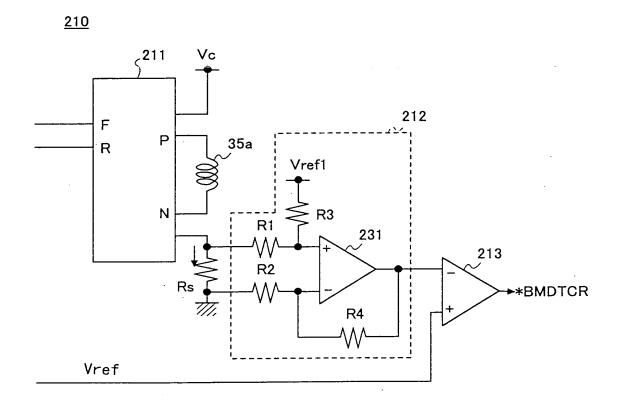




FIG. 5



APPROVED Q.G. FIG. DRAFTSMAN



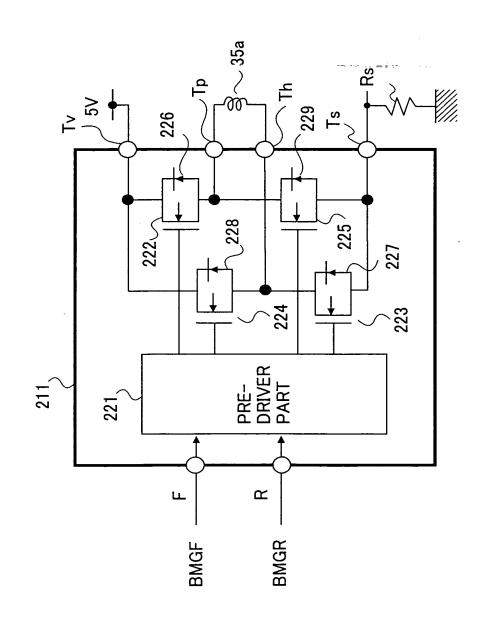


FIG.6

APPROVED O.G. FIG. DRAFTSMAN

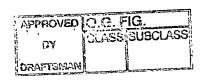
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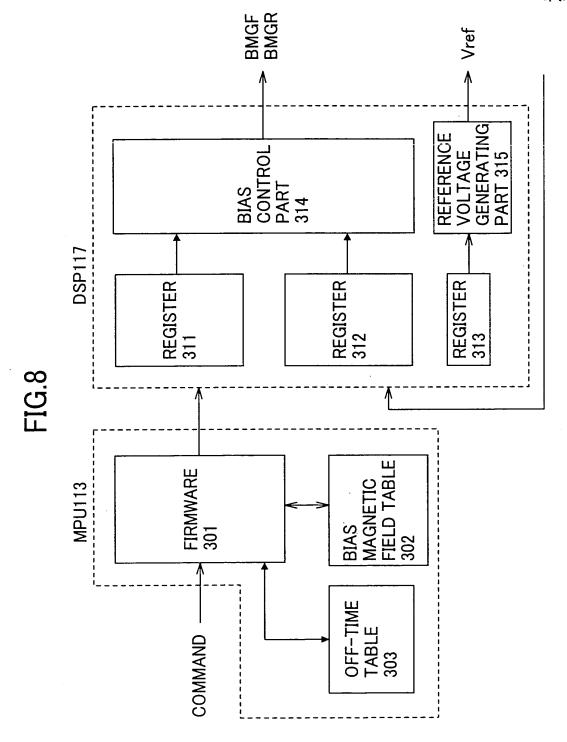
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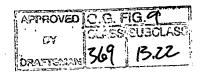
NOTE	POSITIVE CHANGE(P→N)	NEGATIVE CHANGE(N-→P)	SHORT BREAK
N OUTPUT		エ	
P OUTPUT N OUTPUT	工		
BMGR		工	
BMGF	エ		



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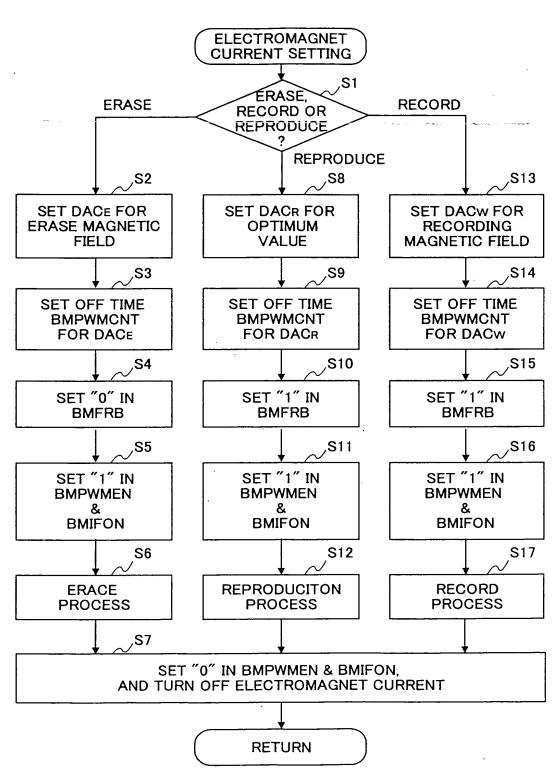


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FIG.9



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FIG.10A

	ELECTROMAGNET ELECTRIC CURRENT			
ZONE NUMBER	ERASE	RECORD	INITIAL VALUE FOR REPRODUCTION	CALIBRATION COEFFICIENT
Z1			IR_Z1[mA](DACR_Z1)	α1
Z2			IR_Z2[mA](DACR_Z2)	α2
Z3		Iw[mA] (DACw)	IR_Z3[mA](DACR_Z3)	α3
Z4			IR_Z4[mA](DACR_Z4)	α4
Z5	IE[mA] (DACE)		IR_Z5[mA](DACR_Z5)	α5
Z6			IR_Z6[mA](DACR_Z6)	α6
Z 7			IR_Z7[mA](DACR_Z7)	α7
Z8			IR_Z8[mA](DACR_Z8)	α8
Z9			IR_Z9[mA](DACR_Z9)	α9
Z10			IR_Z10[mA](DACR_Z10)	α10
Z11			IR_Z11[mA](DACR_Z11)	α11

FIG.10B

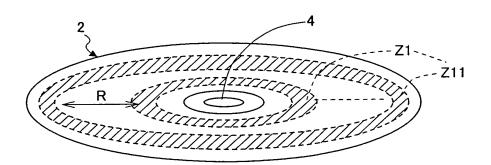


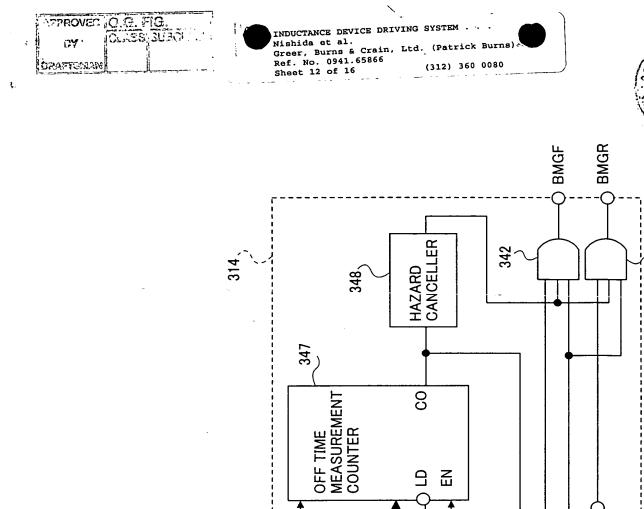




FIG. 11

<u>303</u>

BLAS_DAC	BMPWMCNT	
0x00~0x0f	0x18	
0x10~0x1f	0x10	
0x20~0x3f	. 0×08	
0x40~0x7f	0×04	
0x80~0xff	0×01	



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343

345

344

REGISTER 312 BMFRB Ċ BMIFON ○

341

SYN. FF

COMPARISON CIRCUIT213 *BMDTCR 🖒

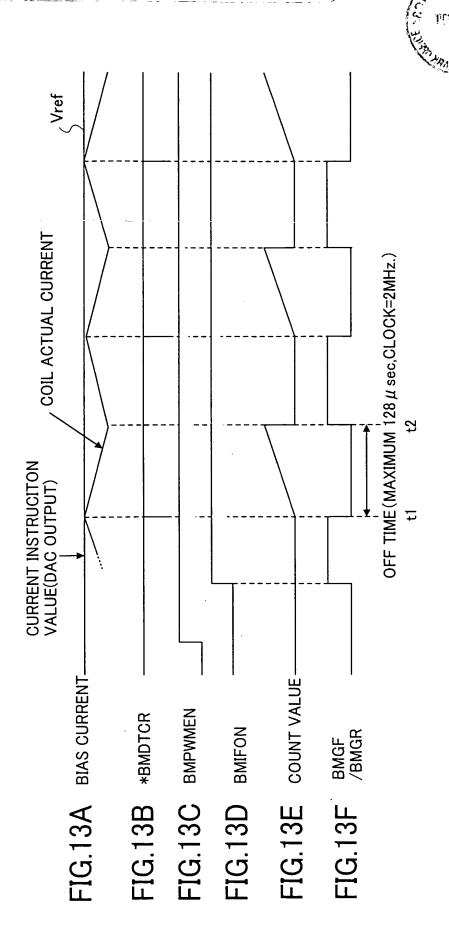
BMPWMEN ¢

346

8BITS

REGISTER 311 BMPWMCNT C

FIG.12



ORATIONAM C. F.G.

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FIG.14

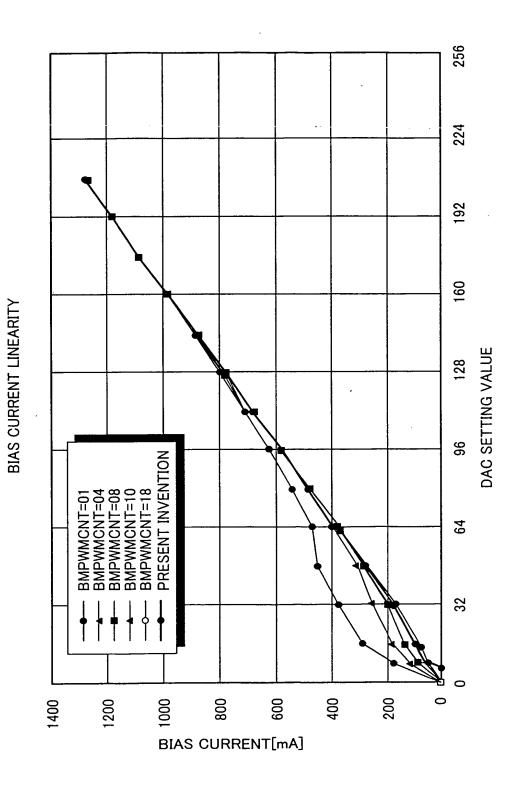
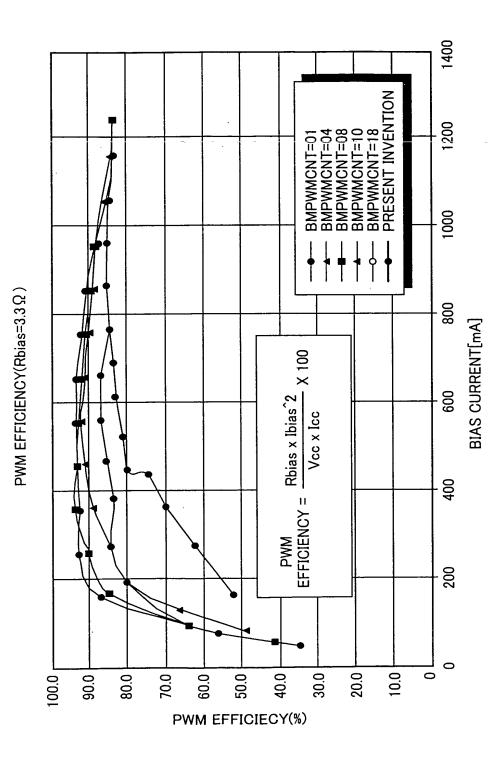
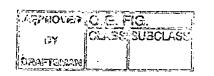




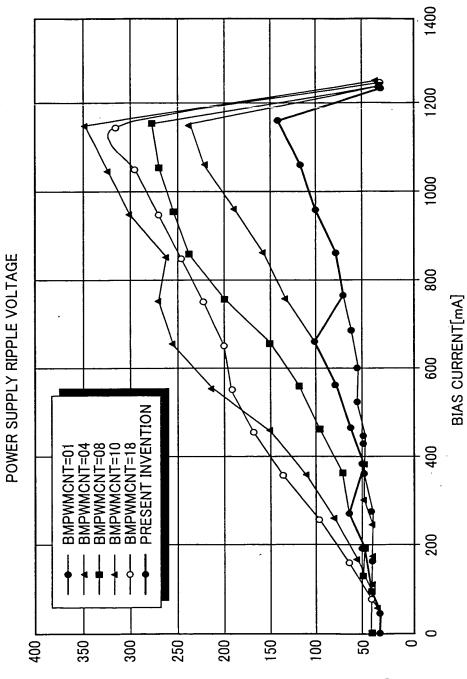
FIG.15





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POWER SUPPLY RIPPLE VOLTAGE[mV]